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		OTHER DOCUME		-	, Title, Date, Pertir	_				
DK	International Business Machines Corporation, "Hybrid Transistional Coding of Wide On-Chip Busses", Research Disclosure, Nov. 2001, pp. 1958-1959.									
D16	Nalamalpu et al., "Boosters For Driving Long Onchip Interconnects-Design Issues, Interconnect Synthesis, and Comparison with Repeaters", IEEE Xplore, Vol. 21, No. 1, January 2002, pp. 50-62.							nparison		
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	L	OTHER DOCUME	•	-	Title, Date,					
DK9	Yamada et al., "A Novel Approach to Realizing Flexible Transport Systems Using Reconfigurable Hardware", IEEE, Vol. 1, November 1995, pp. 67-71.							, Vol. 1,		
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Electronic Version v18

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07/01/03

Title of
Invention

CIRCUIT AND METHOD FOR PIPELINED INSERTION

Application Number:

10/604205

Confirmation Number:

First Named Applicant:

Robert Horton

Attorney Docket Number:

BUR920030019US1

Art Unit:

2811

Examiner:

Owens

Search string:

(5369640 or 5930525 or 6018782 or 6035364 or 6275975 or 6279142 or 6415344

or 6426656 or 20020161959 or 20020175390).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
90	1	5369640	1994-11-29	Watson et al.			
90	2	5930525	1999-07-27	Gotesman et al.			
44	3	6018782	2000-01-25	Hartmann			
44	4	6035364	2000-03-07	Lambrecht et al.			
47	5	6275975	2001-08-14	Lambrecht et al.			
91	6	6279142	2001-08-21	Bowen et al.			
91	7	6415344	2002-07-02	Jones et al.			
qV	8	6426656	2002-07-30	Dally et al.			

US Published Applications

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91	1	20020161959	2002-10-31	Apostol, Jr. et al.			
94	2	20020175390	2002-11-21	Craft			

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